# Time to Failure Analysis of Single Mode Longwavelength InGaAsP Vertical-Cavity Surface-Emitting Lasers

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Abstract— In this paper, we investigate the time to failure (tf) analysis of a Single Mode 1.55  $\mu$ m InGaAsP vertical cavity surface emitting laser (VCSEL) with two different electrical confinement structures. The electrical confinement introduced by the oxide aperture or air-posted design are compared with conventional structure. Air-posted VCSEL shows an average of 7.43X and 19.83X Improvement in time to failure than that of the similar oxide confined and conventional VCSEL, respectively. This paper provides key results of the device characteristics, including the DC V-I, junction temperature as a function of the optical power and the time to failure vs. junction temperature. Results suggest that the 1.55  $\mu$ m InGaAsP air-posted VCSEL seems to be the most optimal choice for light sources in high reliability optical communication systems.

## Keywords- Time to failure; InGaAsP; Long-wavelength; VCSEL;

#### I. INTRODUCTION

In recent years, the vertical cavity surface emitting lasers (VCSELs) have attracted extremely [1]. VCSEL is one of the key light sources used in high performance optical communication systems where single mode operation, high output power, high speed modulation and low manufacturing cost are necessary [2]. High optical gain in the active area, high temperature, low threshold current and high thermal conductivity in the reflecting mirrors are the main difficulties in developing VCSELs which are used in the field of optical spectroscopy [3]. When all lateral modes except one are suppressed, a fully single mode VCSEL operation is achieved. Mainly, a single lobe distribution in a desirable lateral direction defines lasing of the fundamental mode. If carriers are confined to the central part of the device, this modal distribution has best overlap with the gain's radial distribution. In a real device, the injected current has distributed over all of active region while the fundamental mode has appreciable amplitude in the center. Consequently, it is necessary to funnel the current into the center of the active region. There have been several approaches to address this issue: selective oxidation [4], proton implantation [5] or structured tunnel junction [6]. On the one hand, narrowing the current aperture causes the reduction of the emitted power and on the other hand, broadening of the current aperture favors multimode operation. Hence, it is necessary to use additional structuring of VCSELs for their single mode operation with broader current apertures. This can be assured by anti-resonant profile of the refractive index in the distributed Bragg reflectors (DBRs) [7], surface etching [8], surface grating [9] and photonic crystals (PhC) [10].

There are several reasons why the development of highpower InP-based VCSELs has not been so successful within these years as follows: 1) Need to develop a high-efficiency bottom-emitting single-device; 2) Need to improve the growth and process uniformity for cm-scale chips; 3) Need to improve the packaging for reliability and kW-level heat-removal [11]. While work on the reliability and time to failure of GaInNAs and InAlGaAs oxide confined and proton-implanted VCSELs on GaAs and InP substrates has been done [12, 13], the reliability and time to failure of InGaAsP air-posted VCSEL developed on GaAs has never been reported.

In this paper, the impact of the oxide confined (OC) and air-posted electrical confinement schemes on the time to failure of Single Mode 1.55 um InGaAsP VCSEL are investigated and compared with conventional VCSEL. The paper is organized as follows: Section II briefly describes the theoretical model and discusses the physics of failure for the VCSELs, Sec. III provides the details of the VCSEL structures, and Sec. IV presents the obtained numerical results. Finally, in Sec. V, we conclude the paper.

#### II. THEORY

In modeling VCSEL, we must consider the electrical, optical and thermal interaction during VCSEL performance [14]. Thus, the base of simulation is to solve Poisson and continuity equations for electrons and holes. Poisson's equation is defined by [15]:

$$\nabla \bullet (\varepsilon \nabla \Psi) = \rho \tag{1}$$

where  $\psi$  is electrostatic potential,  $\rho$  is local charge density and  $\varepsilon$  is local permittivity. The continuity equations of electron and hole are given by [15]:

$$\frac{\partial n}{\partial t} = G_n - R_n + \frac{1}{q} \nabla \vec{J}_n \tag{2}$$

$$\frac{\partial p}{\partial t} = G_p - R_p + \frac{1}{q} \nabla \vec{J}_p \tag{3}$$

where n and p are the electron and hole concentrations,  $J_n$  and  $J_p$  are the electron and hole current densities,  $G_n$  and  $G_p$  are the generation rates for electrons and holes,  $R_n$  and  $R_p$  are the recombination rates and q is the magnitude of electron charge.

The fundamental semiconductor equations (1)-(3) are solved self-consistently together with Helmholtz and the photon rate equations. The applied technique for solution of Helmholtz equation is based on effective frequency method [16]. Two-dimensional Helmholtz equation is solved to determine the transverse optical field profile and is given by [15]:

$$\nabla^2 E(r, z, \varphi) + \frac{\omega_0}{c^2} \varepsilon(r, z, \varphi, \omega) E(r, z, \varphi) = 0$$
(4)

where  $\omega$  is the frequency,  $\epsilon(r, z, \phi, \omega)$  is the complex dielectric permittivity,  $E(r, z, \phi)$  is the optical electric field and c is the speed of light in vacuum.

The light power equation relates electrical and optical models. The photon rate equation is given by [15]:

$$\frac{dS_m}{dt} = \left(\frac{c}{N_{eff}}G_m - \frac{1}{\tau_{ph_m}} - \frac{cL}{N_{eff}}\right)S_m + R_{sp_m}$$
(5)

where  $S_m$  is the photon number  $,G_m$  is the modal gain,  $R_{spm}$  is the modal spontaneous emission rate, L represents the losses in the laser,  $N_{eff}$  is the group effective refractive index,  $\tau_{phm}$  is the modal photon lifetime and c is the speed of light in vacuum. The m refers to the modal number.

The heat flow equation has the form [15]:

$$C\frac{\partial T_L}{\partial t} = \nabla(\kappa \nabla T_L) + H \tag{6}$$

where C is the heat capacitance per unit volume,  $\kappa$  is the thermal conductivity,  $T_L$  is the local lattice temperature and H is the heat generation term.

The heat generation equation has the form [15]:

$$H = \left[\frac{\overline{\left|J_{n}\right|^{2}}}{q\mu_{n}n} + \frac{\overline{\left|J_{p}\right|^{2}}}{q\mu_{p}p}\right] + q(R-G)\left[\phi_{p} - \phi_{n} + T_{L}(P_{p} - P_{n})\right]$$
(7)  
$$-T_{L}\left(\overline{J_{n}}\nabla P_{n} + \overline{J_{p}}\nabla P_{p}\right)$$

Where:  $\left[\frac{\overline{|J_n|}^2}{q\mu_n n} + \frac{\overline{|J_p|}^2}{q\mu_p p}\right]$  is the Joule heating term,  $q(R-G)[\phi_p - \phi_n + T_L(P_p - P_n)]$  is the recombination and

generation heating and cooling term,  $-T_L(\overrightarrow{J_n}\nabla P_n + \overrightarrow{J_p}\nabla P_p)$  accounts for the Peltier and Thomson effects.

In addition, all important, usually non-linear, interactions between the optical, electrical, thermal and recombination phenomena are also taken into account with the aid of the selfconsistent iteration algorithm, including: gain-induced waveguiding; thermal focusing; self-focusing; temperature dependence of optical gain, absorption coefficients, the energy gaps, electrical conductivities, thermal conductivities; carrierconcentration dependence of electrical

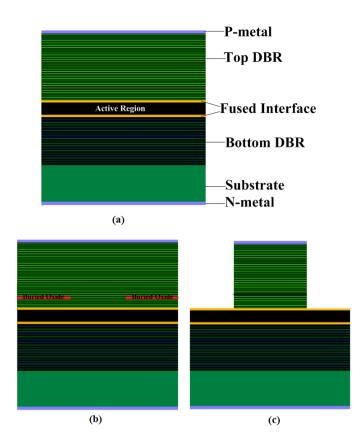


Figure 1. Schematic structure of the double-fused VCSEL analyzed: a) Conventional b) Oxide confined c) Air-posted VCSEL

Conductivities, optical gain, absorption coefficients and the energy gaps; wavelength dependences of optical gain and absorption coefficients. Eq. (1)-(7) provide an approach that can account for the mutual dependence of electrical, thermal, optical and elements of heat sources. In this paper, we employ ATLAS numerical-based simulation software to assist as in the device design and simulation [15].

#### III. VCSEL STRUCTURE STUDIED

The analyzed structure is similar to the one recently reported in [17-18] has been chosen as a model structure for the analysis of the 1.55  $\mu m$  InGaAsP VCSEL. The active region consists of six quantum wells where the well is 5.5 nm  $In_{0.76}Ga_{0.24}As_{0.82}P_{0.18}$  and the barrier is 8 nm  $In_{0.48}Ga_{0.52}As_{0.82}P_{0.18}$ . In both sides of this active region, there is InP and there is GaAs on the top. The top mirror is 30 layers of GaAs/Al<sub>0.33</sub>Ga<sub>0.67</sub>As with index of refraction of 3.38 and 3.05 respectively, and the bottom mirror has 28 layers of GaAs/AlAs with index of refraction of 3.38 and 2.89, respectively (Structure A). In the OC VCSEL, the incorporation of a high aluminum content layer (Al<sub>0.98</sub>Ga<sub>0.02</sub>As) in two DBR periods above the active region allows for selective oxidation [19] as shown Fig.1 (structure B). Another confinement solution that takes advantage of the index guiding is that of air-post VCSELs as demonstrated shown Fig.1 (structure C). The layer thickness, doping, majority carrier mobility, refractive index, temperature coefficient of n, absorption coefficient, and thermal conductivity of the layers the device simulated are listed in Table for 1.

TABLE I. Parameters of the structure (I layer thickness,  $N_{dop}$  doping,  $\mu$  majority carrier mobility, n refractive index, dn/dT temperature coefficient of n,  $\alpha$  absorption coefficient, and  $\kappa$  thermal conductivity.)

| Parameter<br>Unit                                                                    | /<br>(μm)      | N <sub>dop</sub><br>(1/cm <sup>3</sup> ) | μ<br>(cm <sup>2</sup> /V s) | n            | dn/dT<br>(10 <sup>-4</sup> /K) | α<br>(1/cm)   | к<br>(W/cm K) |
|--------------------------------------------------------------------------------------|----------------|------------------------------------------|-----------------------------|--------------|--------------------------------|---------------|---------------|
| Au/Ti (Contact)<br>p-GaAs                                                            | 0.200<br>0.020 | 2×10 <sup>19</sup>                       |                             | 0.83<br>3.38 |                                | 684000<br>500 | 0.67<br>0.44  |
| <i>p</i> -GaAs                                                                       | 0.182          | $4 \times 10^{17}$                       |                             | 3.38         | 3                              | 25            | 0.22          |
| <i>p</i> -Al <sub>0.67</sub> Ga <sub>0.33</sub> As (DBR)                             | 0.127          | 4×10 <sup>17</sup>                       |                             | 3.05         | 2                              | 25            | 0.22          |
| <i>p</i> -GaAs (DBR)                                                                 | 0.115          | 4×10 <sup>17</sup>                       |                             | 3.38         | 3                              | 25            | 0.44          |
| <i>p</i> -GaAs (Spacer)                                                              | 0.020          | 4×10 <sup>17</sup>                       |                             | 3.38         | 3                              | 25            | 0.44          |
| <i>p</i> -GaAs (Spacer)                                                              | 0.010          | 4×10 <sup>19</sup>                       |                             | 3.38         | 3                              | 1000          | 0.44          |
| <i>p</i> -InP (Spacer)                                                               | 0.178          | 1×10 <sup>18</sup>                       | 30                          | 3.17         | 2                              | 24            | 0.68          |
| <i>p</i> -Inp (Spacer)                                                               | 0.100          | 1×10 <sup>16</sup>                       | 150                         | 3.17         | 2                              | 0.24          | 0.68          |
| In <sub>0.76</sub> Ga <sub>0.24</sub> As <sub>0.82</sub> P <sub>0.18</sub> (QW)      | 0.0055         |                                          | 100                         | 3.6          | 2                              | 54            | 0.043         |
| In <sub>0.48</sub> Ga <sub>0.52</sub> As <sub>0.82</sub> P <sub>0.18</sub> (Barrier) | 0.008          |                                          | 100                         | 3.4          | 2                              | 54            | 0.043         |
| <i>n</i> -InP (Spacer)                                                               | 0.258          | 5×10 <sup>18</sup>                       | 4600                        | 3.15         | 2                              | 8             | 0.68          |
| <i>n</i> -GaAs (Spacer)                                                              | 0.050          | 1×10 <sup>18</sup>                       |                             | 3.38         | 3                              | 6             | 0.44          |
| <i>n</i> -GaAs (DBR)                                                                 | 0.115          | 1×10 <sup>18</sup>                       |                             | 3.38         | 3                              | 6             | 0.22          |
| <i>n</i> -AlAs (DBR)                                                                 | 0.134          | 1×10 <sup>18</sup>                       |                             | 2.89         | 1                              | 3             | 0.22          |
| <i>n</i> -GaAs (Substrate)                                                           | 450            | 5×10 <sup>18</sup>                       |                             | 3.38         | 3                              | 5.8           | 0.44          |

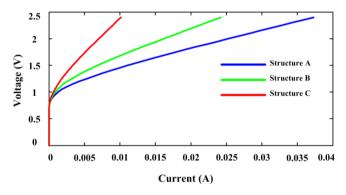
### IV. RESULTS

The present work allows for determination of the optimum VCSEL structure that is found by comparison of the time to failure. Heat loss from the modeled VCSEL device was specified using thermal contacts at the top electrode, bottom electrode and the device sidewall. The thermal contacts define thermal conductivities to simulate heat loss from radiation via exposed surfaces or conduction through the semiconducting material to a heat-sink.

Fig.2 shows the voltage versus current curve for different structures. The DC V-I characteristics exhibit that the air-post structure has higher series resistance, which should be mainly due to etching on the top DBR and the surface recombination of carrier concentration. Due to the blocking of the current flow in the active region, oxide confined and air-post structures exhibit extremely low threshold current in comparison with the conventional structure [20].

Fig.3 shows the junction temperature as a function of the optical power. As seen, the air-post structure has a higher junction temperature at constant optical power. Due to the removal of the semiconductor surrounding the etched mesa, and the fact that the current must pass through a smaller constriction, the thermal resistance is high.

Reliability is measured through Accelerated Life Testing (ALT) that employs the widely accepted modified erying electromigration stress acceleration model. This model





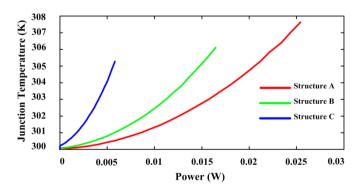


Figure 3. Junction temperature as a function of optical power.

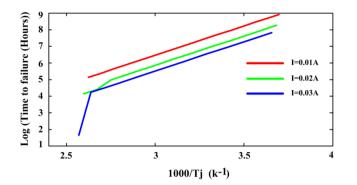


Figure 4. Logarithmic time to failure of conventional structure as a function of junction temperature for different currents.

includes current density and arrehenius temperature acceleration factors and is given by [21]:

$$tf = AJ^{-N} \exp(\frac{Ea}{K_B T})$$
(8)

where tf is time to failure, A is a scaling factor, J is current density, N is current acceleration factor that is usually between 2-3 (2 in this analysis), Ea is activation energy (0.7 eV in this analysis),  $K_B$  is the boltzman's constant, and T is VCSEL junction temperature.

The time to failure of different structures as a function of junction temperature for different current at room temperature are logarithmically plotted in Figs.4-6. As can be seen, unlike some other semiconductor lasers, VCSEL degradation is linear. For comparison, Fig.7 shows the logarithmic time to failure of different structures at constant voltage. As seen, the air-post structure exhibits a remarkable improvement of time to failure of more than 7X compared to the other structures. Although this structure has a higher junction temperature, its much lower current yields this improvement. (i.e. 303.4 and 0.006 A for the air-post; and 302.5 K and 0.016 A for oxide confined VCSEL at 2 V). As seen, air-posted VCSEL shows an average of 7.43X and 19.83X improvement in time to failure compared to similar oxide confined and conventional VCSEL, respectively. Therefore, air-posted VCSEL seems to be the most optimal choice for light sources in high reliability optical communication systems.

#### V. CONCLUSION

Time to failure of a Single Mode 1.55µm InGaAsP vertical cavity surface emitting laser with two different electrical confinement structures was analyzed and compared with conventional structure. Air-posted VCSEL shows higher time to failure than that of the similar oxide confined and conventional VCSEL. In summary, the results indicate that the air-post structure exhibit remarkable improvement in time to failure for light sources and is suitable for use in high reliability optical communication systems.

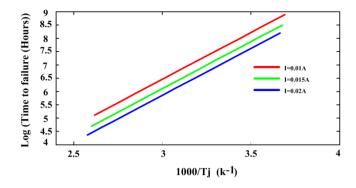


Figure 5. Logarithmic time to failure of oxide confined structure vs junction temperature for different currents.

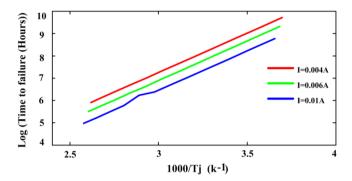


Figure 6. Logarithmic time to failure of air-posted structure as a function of junction temperature for different currents.

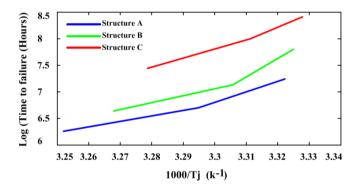


Figure 7. Comparing the logarithmic time to failure of different structures at constant voltages.

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