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Mohammad-Sadegh Karbasforooshan & Mohammad Monfared

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# Multi-Resonant Indirect Digital Current Control Technique for Single-Phase Shunt Active Power Filters

Mohammad-Sadegh Karbasforooshan and Mohammad Monfared

Department of Electrical Engineering, Faculty of Engineering, Ferdowsi University of Mashhad, Mashhad, Iran

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**Abstract**—This article proposes multi-resonant indirect digital current control technique for single-phase shunt active power filters. The main advantages of the suggested technique are simplicity, fast dynamics, low computational burden, and low current ripple. A straightforward design technique in the frequency domain is proposed to properly decide the controller gains and study its performance. Then, the digital implementation of the proposed control algorithm is presented, which lets implement it on a mid-range digital signal controller. Simulation and experimental results on a prototype system are reported to demonstrate the effectiveness of the proposed control system.

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## 1. INTRODUCTION

Active power filters (APFs) have been introduced to alleviate the effects of grid current harmonics caused by nonlinear loads on the utility grid. These loads, which are rapidly growing, draw harmonic currents from the grid and distort the voltage and cause many other problems. The APFs can be used in shunt or series structure with the grid to compensate the harmonic currents and prevent the voltage from being distorted. Up to now, many different control techniques for the single-phase shunt APFs have been suggested. From these, one can point to the discrete Fourier transform (DFT) and its derivatives [1–3], instantaneous active and reactive power theory (PQ theory) and its derivatives [4–6], synchronous reference frame (SRF) method [7–9], repetitive control [10–11], sliding mode,  $H_\infty$  and nonlinear controls [12–15], neural network and fuzzy techniques and etc. [16–21]. These techniques have the common advantages such as precise detection of the fundamental and harmonic components, fast dynamic response, and selective harmonic compensation. But, complexity, high computational burden and low switching frequency (high current ripple) may be some limitations.

Keywords: indirect control, single-phase shunt active power filter, digital implementation, multi-resonant controller, proportional current controller, DC-link voltage controller, bandwidth frequency, harmonic compensation, fast dynamics, low computational burden

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Address correspondence to Mohammad Monfared, Department of Electrical Engineering, Faculty of Engineering, Ferdowsi University of Mashhad, Mashhad, Iran. E-mail: [m.monfared@um.ac.ir](mailto:m.monfared@um.ac.ir)

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This article proposes an indirect current control technique for the single-phase shunt APFs. This technique benefits from some advantages of simplicity, fast dynamic response, low computational burden. In the next sections, first the proposed technique is introduced and then the design details with digital implementation are discussed. Simulation and experimental results are presented, which confirm the proper operation of the proposed technique.

## 2. INDIRECT CURRENT CONTROL OF SINGLE-PHASE SHUNT ACTIVE POWER FILTERS

### 2.1. System Model and Indirect Current Control

Figure 1 shows the structure of a single-phase shunt active power filter. This structure consists of an inductor, a capacitor and four power semiconductor switches. The Kirchhoff's current law in the point of common coupling (PCC) is

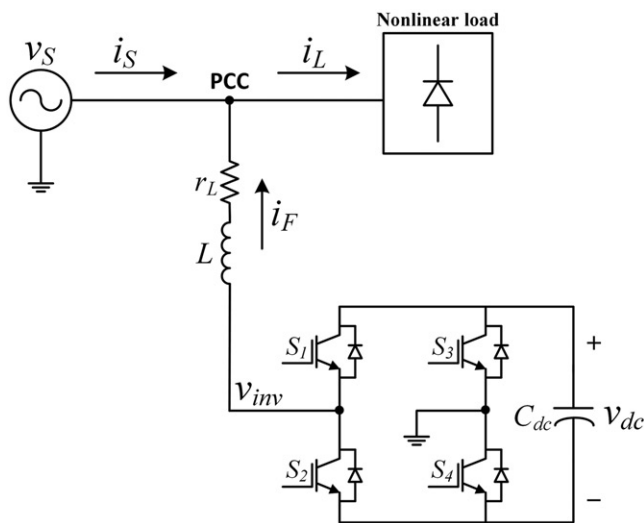


FIGURE 1. Single-phase shunt active power filter.

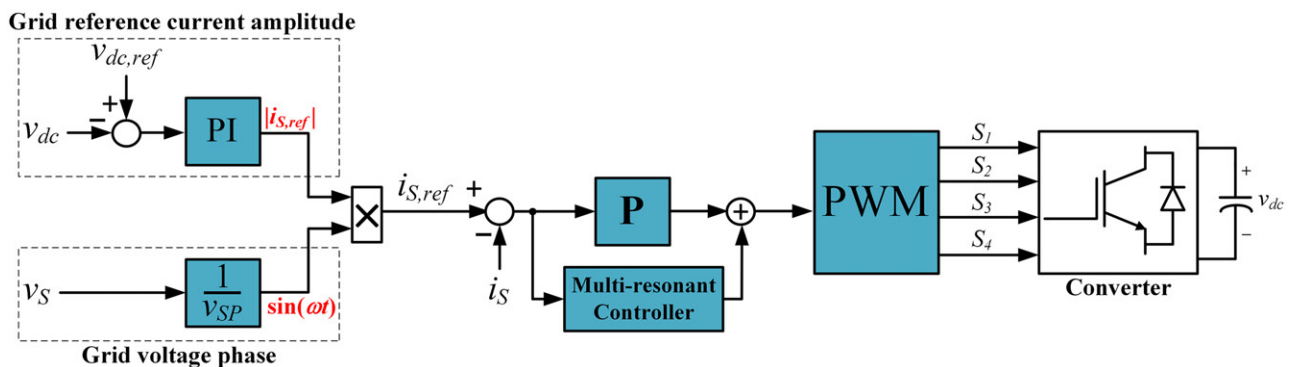


FIGURE 2. Proposed controller for single-phase shunt active power filters.

$$i_S = i_L - i_F \quad (1)$$

where  $i_S$ ,  $i_L$ ,  $i_F$  are source, load, and filter currents, respectively. The inverter output voltage is

$$v_{inv} = r_L i_F + L \frac{di_F}{dt} + v_S \quad (2)$$

This equation can be rewritten as

$$\left( L \frac{d}{dt} + r_L \right) i_F = v_{inv} - v_S \quad (3)$$

By applying Laplace operator to (3), one has

$$I_F(s) = \frac{1}{Ls + r_L} (V_{inv}(s) - V_S(s)) \quad (4)$$

Figure 2 shows the block-diagram of the proposed indirect current control for the single-phase shunt active power filters. This technique works with two voltage sensors and only one current sensor. The amplitude of the grid reference current is calculated by passing the difference of sensed DC-link voltage and its reference value through a proportional integral (PI) controller. The phase of the grid voltage, i.e.  $\sin(\omega t)$ , is easily obtained by dividing the sensed grid voltage by the voltage amplitude (the amplitude is not necessarily exact). By multiplying the amplitude and the phase of the grid reference current and by having a simple proportional gain, the final control signal to be compared with a carrier, is generated. Also, it is proposed to use a network of resonant controllers in parallel with the proportional controller to highly attenuate the specific harmonics of interest.

### 2.2. Proportional Current Controller Design

In order to design proportional current controller, the transfer function of the filter current loop should be obtained. Therefore, we have

$$G_I(s) = \frac{I_F(s)}{I_{F,ref}(s)} = \frac{P}{Ls + (r_L + P)} \quad (5)$$

By considering  $-3\text{dB}$  attenuation of (5) at the desired current bandwidth frequency, one has

$$\frac{P^2}{(r_L + P)^2 + (L\omega_{bi})^2} = \frac{1}{2} \quad (6)$$

Therefore, the proportional current controller is obtained as

$$P = r_L + \sqrt{2r_L^2 + L^2\omega_{bi}^2} \quad (7)$$

Because the load current has a negligible dynamic, this proportional gain can be used in the source current loop (in Figure 2), also. The current control bandwidth of the active power filter should be chosen enough higher than the largest harmonic frequency, which is decided to be effectively compensated, in order to provide enough control action (gain) up to that harmonic frequency. Also, the bandwidth must be chosen adequately less than the switching frequency to prevent the switching harmonics (noises) to affect the current control performance. Therefore, the current control bandwidth selection is a tradeoff between the reference tracking and the attenuation of the switching frequency components. So, the current control bandwidth frequency is selected in the range of one-fifth to one-third of the switching frequency.

### 2.3. DC-Link Voltage Controller Design

By neglecting the loss occurring in power semiconductors, one can assume that the average power at the inverter DC side ( $P_{dc}$ ) is equal to the average power at the AC side ( $P_{ac}$ ) of the single-phase APF. So, based on Figure 1, we have

$$\frac{1}{T} \int_0^T V_{S,max} i_{F,max} \sin^2(\omega t) d\omega t = i_{dc} v_{dc} \quad (8)$$

where  $V_{S,max}$  and  $i_{F,max}$  are the peak values of the grid voltage (which assumed to be constant) and active fundamental current absorbed by the APF (which is in-phase with the grid voltage), respectively. By simplification, (8) will be

$$-\frac{1}{2} V_{S,max} i_{F,max} = C_{dc} \frac{dv_{dc}}{dt} v_{dc} = \frac{1}{2} C_{dc} \frac{dv_{dc}^2}{dt} \quad (9)$$

Applying the small-signal linearization to (9), yields

$$-\frac{1}{2} V_{S,max} (i_{F,max} + \hat{i}_{F,max}) = \frac{1}{2} C_{dc} \frac{d}{dt} \left( V_{dc}^2 + 2V_{dc} \hat{v}_{dc} + \hat{v}_{dc}^2 \right) \quad (10)$$

The DC components of (10) and the second-order perturbations are assumed to be zero. So, the AC components are

$$-\frac{1}{2} V_{S,max} \hat{i}_{F,max} = C_{dc} V_{dc} \frac{d\hat{v}_{dc}}{dt} \quad (11)$$

Assuming that the load current has a negligible dynamic behavior,  $-i_{F,max}$  can be replaced by  $i_{S,max}$ . By applying the Laplace operator to (11), the result becomes

$$G_{v_{dc}}(s) = \frac{\hat{v}_{dc}(s)}{\hat{i}_{S,max}} = \frac{V_{S,max}}{2C_{dc}V_{dc}s} \quad (12)$$

The closed-loop transfer function of DC-link voltage by noting to Figure 2 is obtained as

$$\frac{v_{dc}(s)}{v_{dc,ref}(s)} = \frac{V_{S,max}(K_{PS} + K_I)}{2C_{dc}V_{dc}s^2 + V_{S,max}(K_{PS} + K_I)} \quad (13)$$

Tuning the PI controller is a tradeoff between accessible control bandwidth and the loop stability. The effect of the integral part of PI controller around crossover frequency can be neglected. So, (13) without integral gain simplifies to

$$\frac{v_{dc}(s)}{v_{dc,ref}(s)} \Big|_{K_I=0} = \frac{V_{S,max}K_P}{2C_{dc}V_{dc}s + V_{S,max}K_P} \quad (14)$$

Considering  $-3\text{dB}$  attenuation of (14) at the DC-link voltage bandwidth frequency, gives the proportional gain of DC-link voltage controller as

$$K_P = \frac{2C_{dc}V_{dc}\omega_v}{V_{S,max}} \quad (15)$$

The voltage bandwidth frequency is selected below one-tenth the grid voltage frequency. The integral part of the DC-link PI controller is tuned according to the symmetrical optimum (SO) theory. So,  $K_I$  is obtained as

$$K_I = \frac{2C_{dc}V_{dc}\omega_v^2}{V_{S,max}b} \quad (16)$$

where  $b$  is a design parameter which determines the system phase margin (PM) as

$$\text{PM} = \tan^{-1} \left( \frac{b^2 - 1}{2b} \right) \quad (17)$$

Usually, due to digital system and un-modelled delays, the PM is selected above  $45^\circ$ . In this article, we select the PM equal to  $70^\circ$ .

In order to discretize the DC-link voltage controller, used from Backward Euler discretization method. In this method, the integral part of the PI controller is defined as

$$G_{PI}(z) = K_P + K_I \frac{T_{smp} z}{z - 1} \quad (18)$$

where  $T_{smp}$  is the sampling time of the digital system. Also, the difference equation of the DC-link voltage control which can be used for digital signal controller (DSC) is

$$i_{S,ref\_abs}[k] = i_{S,ref\_abs}[k-1] + (K_P + K_I T_{smp})(V_{dc,ref}[k] - V_{dc}[k]) \quad (19)$$

where  $i_{S,ref\_abs}$  is the output signal of the DC-link voltage controller.

### 3. MULTI-RESONANT CURRENT CONTROL

Resonant controllers are the family of controllers that have a high gain at the reference signal frequency and its multiples. The transfer function of an ideal resonant controller is

$$G_{R,ideal}(s) = \frac{Ks}{s^2 + (n\omega)^2} \quad (20)$$

Figure 3(a) shows the bode diagram of (20) with  $K=20$ ,  $\omega = 100\pi$  rad/s, and  $n \in \{1,3,5\}$ . As can be seen in this figure, the resonant controller has very high (infinite) gain (above  $10^6$ ) with a very low bandwidth. This gain can lead to instability of the controller. So, to overcome on this problem, the nonideal resonant controller is defined as follows

$$G_{R,nonideal}(s) = \frac{Ks}{s^2 + \omega_c s + (n\omega)^2} \quad (21)$$

where  $\omega_c$  is the bandwidth around the frequency of  $n\omega$ . Figure 3(b) shows the bode diagram of (21) with  $K=20$ ,  $\omega = 100\pi$  rad/s,  $n \in \{1,3,5\}$ , and  $\omega_c = 10$  rad/s.

It should be noted that the pass-band of each resonant controller is required to be small enough to ensure that each controller responds to the frequencies around its resonant frequency and has a very negligible effect on the stability margin and dynamic performance of the APF.

According to the high-percentage of the lower-order harmonics in the load current and voltage waveforms, only the resonant controllers at the third, fifth, seventh, and ninth orders are used in this article. A resonator at the fundamental frequency is also added to precisely compensate for the reactive fundamental component of the current. The gain  $K$  for each of these resonant controllers is selected as  $K_h = \{2, 6, 10, 14, 18\} \times \omega$ ,  $k \in \{1, 3, 5, 7, 9\}$ . The parameter  $\omega_c$  is selected to be equal 12 rad/s, which is a compromise between the stability and harmonic rejection capability.

In order to discretize multi-resonant controllers, zero-order hold (ZOH) method which is a suitable discretization

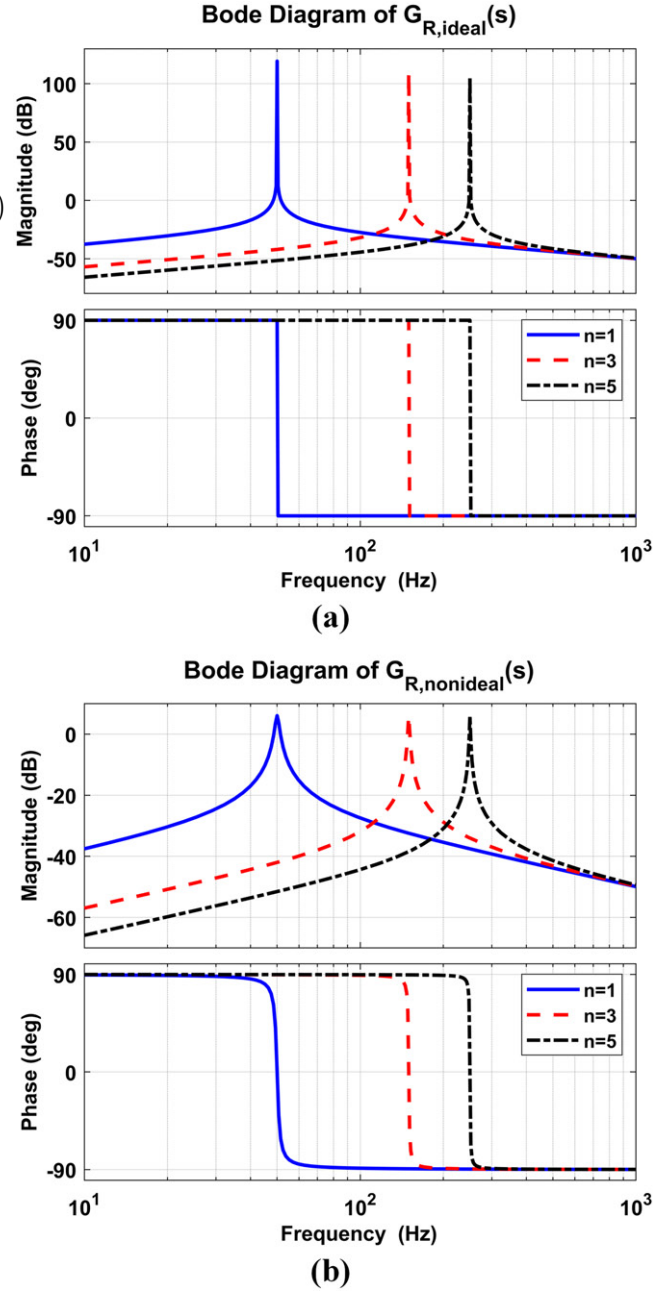


FIGURE 3. Bode diagram of the resonant controller with  $K=20$ ,  $\omega=100\pi$  and  $n \in \{1,3,5\}$ ; (a) ideal resonator, and (b) nonideal resonator.

technique, is adopted. By this method, the resulted digital resonator will be

$$G_{R,nonideal}(z) = \frac{Ke^{-\frac{\omega_c}{2}T_{smp}} \sin(\omega_1 T_{smp})}{\omega_1} \times \frac{z-1}{z^2 - 2ze^{-\frac{\omega_c}{2}T_{smp}} \cos(\omega_1 T_{smp}) + e^{-\omega_c T_{smp}}} \quad (22)$$

where  $\omega_1$  is

$$\omega_1 = \sqrt{n^2\omega^2 + \frac{\omega_c^2}{4}} \quad (23)$$

The difference equation of one nonideal resonant controller is

$$\begin{aligned} y_{res}[k] = & 2e^{-\frac{\omega_c T_{samp}}{2}} \cos(\omega_1 T_{samp}) y_{res}[k-1] \\ & - e^{-\omega_c T_{samp}} y_{res}[k-2] \\ & + \frac{Ke^{-\frac{\omega_c T_{samp}}{2}} \sin(\omega_1 T_{samp})}{\omega_1} \\ & \times \left( (i_{S,ref}[k-1] - i_S[k-1]) \right. \\ & \left. - (i_{S,ref}[k-2] - i_S[k-2]) \right) \end{aligned} \quad (24)$$

where  $y_{res}$  is the output signal of the resonant controller.

#### 4. SIMULATION AND EXPERIMENTAL RESULTS

In order to confirm theoretical results, simulation and experimental results on a typical system are provided. Simulations are done in MATLAB/Simulink environment. The system parameters are:  $v_S = 220V_{rms}$ ,  $L = 3\text{ mH}$ ,  $r_L = 0.5\Omega$ ,  $C_{dc} = 2.2\text{ mF}$  and  $T_{switching} = 0.0001\text{ s}$ . The experimental setup consisting of DC-link capacitor, a full-bridge IGBT intelligent power module, an inductance filter, measurements and gate drive circuits. The nonlinear load of the

setup consists of a single-phase full-bridge diode rectifier feeding a resistor in parallel with a capacitor. The grid voltage and current and the grid current harmonic spectrums are calculated and displayed by the power quality analyzer, Fluke 435. The control algorithm is implemented on a STM32F407VGT6 digital signal controller from the ST. The grid voltage has total harmonic distortion (THD) equal to 5%. Figure 4 shows the simulated grid voltage and current before current compensation. The load current THD is equal to 33%. When single-phase APF is

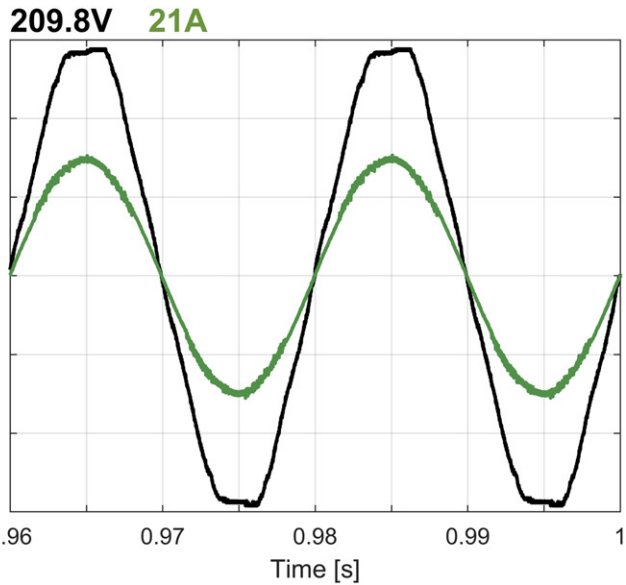


FIGURE 5. Simulated grid voltage (black, 100 V/div) and grid current (green, 20 A/div) after compensation.

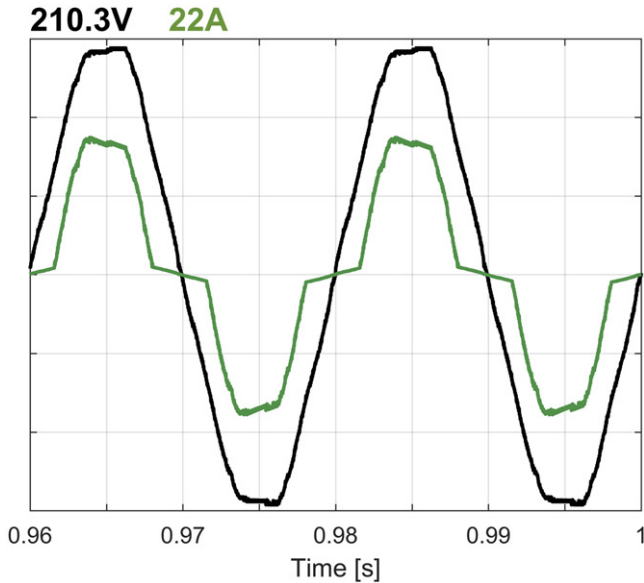


FIGURE 4. Simulated grid voltage (black, 100 V/div) and grid current (green, 20 A/div) before compensation.

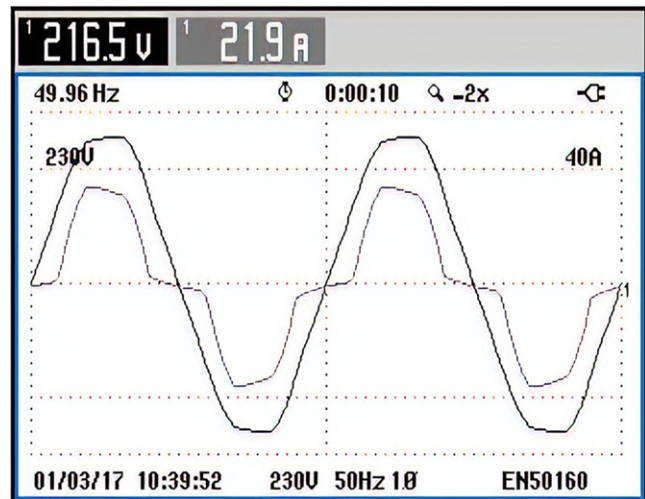


FIGURE 6. Experimental grid voltage and grid current before compensation.

HARMONICS TABLE		
	0:00:13	
Amp	L1	N
THD%f	33.0	253.0
H3%f	30.5	57.7
H5%f	9.7	52.6
H7%f	7.4	53.4
H9%f	2.7	53.4
H11%f	0.7	49.8
H13%f	1.6	53.8
H15%f	0.8	50.2
01/03/17 10:39:31 230V 50Hz 1Ø EN50160		

FIGURE 7. Grid current harmonic table before compensation.

HARMONICS TABLE		
	0:00:02	
Amp	L1	N
THD%f	4.5	251.2
H3%f	2.5	66.2
H5%f	1.0	62.9
H7%f	2.5	63.3
H9%f	0.7	64.2
H11%f	1.1	64.9
H13%f	1.3	59.9
H15%f	0.5	65.9
01/03/17 10:20:46 230V 50Hz 1Ø EN50160		

FIGURE 9. Grid current harmonic table after compensation.

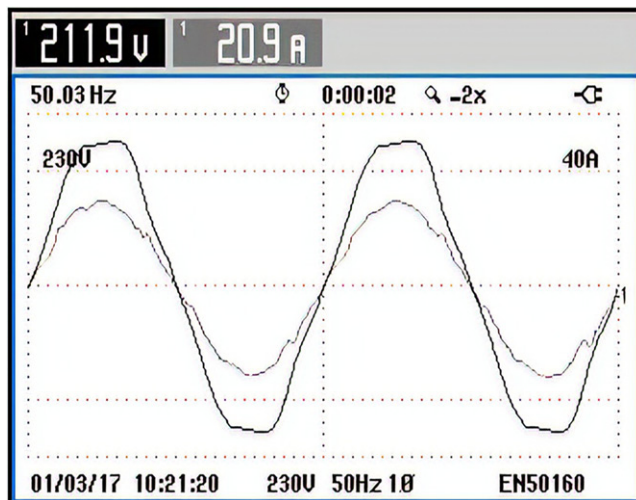


FIGURE 8. Experimental grid voltage and grid current after compensation.

connected to the grid at PCC, the grid current THD decreases to 3.5%. The result is shown in Figure 5. As we can see, the grid current follows the standards limit. Figures 6 and 7 show the experimental grid voltage and current and grid current harmonic table before current compensation, respectively. By connecting APF to the grid, the grid current THD decreases to 4.5% and results are shown in Figures 8 and 9. As shown in these figures, the grid current quality is much improved, the major current harmonics (third, fifth, seventh, and ninth) are well damped, the grid current THD is obtained less than the grid voltage THD and the correct and appropriate performance of the control system is confirmed.

## 5. CONCLUSION

This article proposed a multi-resonant indirect current control method for single-phase shunt APFs. This method has advantages of simplicity, low computational burden and low current ripple. The inner and outer controller design is analyzed in details. Also, the digital representation of the control system is provided to implement on a digital signal controller. Simulation and experimental results on a typical system is provided in this article, also. The results confirm the effectiveness and correct performance of the proposed technique.

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## BIOGRAPHIES

**Mohammad-Sadegh Karbasforooshan** received the B.Sc. and the M.Sc. degrees (both with honors) in electrical engineering from Ferdowsi University of Mashhad, Iran, in 2013, and 2015. He is currently working toward the Ph.D. degree at Ferdowsi University of Mashhad, Iran. His research interests include power electronics, active power filters, and power quality.

**Mohammad Monfared** received the B.Sc. degree in electrical engineering from Ferdowsi University of Mashhad, Iran, in 2004, and the M.Sc. and Ph.D. degrees (both with honors) in electrical engineering from Amirkabir University of Technology, Tehran, Iran, in 2006 and 2010. He is currently an Associate Professor at Ferdowsi University of Mashhad, Iran, where he has received the Best Researcher Award in 2015. His research interests include power electronics, renewable energy systems, and power quality.